

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.
2. Authorization for this examiner's amendment was given in a telephone interview with Rodney Lacy on 1/11/2010.
3. Please replace all prior versions and listing of the application claims with the following:
 1. A method for scheduling a plurality of program units, the method comprising:
starting a process within an operating system executing in a system having a plurality of multiple processor units, each of the multiple processor units having a plurality of processors;
starting the plurality of program units within the operating system, the plurality of program units associated with the process, wherein the plurality of program units execute on two or more of the plurality of multiple processor units; and
upon the occurrence of a context shifting event for a first program unit of the plurality of program units, performing the tasks of:
migrating each of one or more of the plurality of program units that are not executing on a selected multiple processor unit of the plurality of multiple processing units to the selected multiple processor unit,
synchronizing the scheduling of each of the plurality of program units,
setting the context shifting event in a context of each of the plurality of program units to the same context shifting event as the first program unit, and
processing by each of the plurality of program units the same context shifting event;

wherein the context shifting event comprises an exception, a non-local goto, a signal or a system call.

2. The method of claim 1, wherein the program unit comprises a thread.

3.-8. (Cancelled)

9. A system for scheduling a plurality of program units, the system comprising:
a plurality of multiple processor units, each multiple processor unit having a plurality of processors, wherein each of the plurality of processors on a multiple processor unit shares cache memory;

 a memory coupled to the plurality of multiple processor units; and
 an operating environment stored in the memory and executed by at least one of the processors wherein at least one of the processors performs the tasks of:

 start a process,

 start the plurality of program units within an operating system, the plurality of program units associated with the process, wherein the plurality of program units execute on two or more of the plurality of multiple processor units,

 upon the occurrence of a context shifting event for a first program unit of the plurality of program units, at least one of the processors performs the tasks of:

 migrate each of one or more of the plurality of program units that are not executing on a selected multiple processor unit of the plurality of multiple processing units to the selected multiple processor unit,

 synchronize the scheduling of each of the plurality of program units, and
 set the context shifting event in a context of each of the plurality of program units to the same context shifting event as the first program unit,
 wherein each of the plurality of program units process the same context shifting event;

wherein the context shifting event comprises an exception, a non-local goto, a signal or a system call.

10. The system of claim 9, wherein the program unit comprises a thread.

11.-16. (Canceled)

17. A computer storage medium having stored thereon computer-executable instructions ~~for performing that when executed by one or more processors perform a method for scheduling a plurality of program units, the method comprising:~~

starting a process within an operating system executing in a system having a plurality of multiple processor units, each of the multiple processor units having a plurality of processors;

starting the plurality of program units within the operating system, the plurality of program units associated with the process, wherein the plurality of program units execute on two or more of the plurality of multiple processor units; and

upon the occurrence of a context shifting event for a first program unit of the plurality of program units, performing the tasks of:

migrating each of one or more of the plurality of program units that are not executing on a selected multiple processor unit of the plurality of multiple processing units to the selected multiple processor unit,

synchronizing the scheduling of each of the plurality of program units, and

setting the context shifting event in a context of each of the plurality of program units to the same context shifting event as the first program unit, and

processing by each of the plurality of program units the same context shifting event;

wherein the context shifting event comprises an exception, a non-local goto, a signal or a system call.

18. The computer storage medium of claim 17, wherein the program unit comprises a thread.

19.-24. (Canceled)

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to CAROLINE ARCOS whose telephone number is (571)270-3151. The examiner can normally be reached on Monday-Thursday 7:00 AM to 5:30 PM.

5. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Meng-Ai An can be reached on 571-272-3756. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

6. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Meng-Ai An/
Supervisory Patent Examiner, Art Unit 2195

/Caroline Arcos/

Examiner, Art Unit 2195